

UTS Prize Conditions of Award Electric Energy Society of Australia Prize

Faculty: Engineering and Information Technology

This document sets out the conditions of award for the below prize ('Prize') and the obligations of recipients ('Recipient') and UTS in regards to this Prize. The administrative processes to support awarding this Prize will be managed, and may be amended, in accordance with UTS Rules, Policy and Procedures.

1. PRIZE TITLE: Electric Energy Society of Australia Prize

2. PURPOSE

Since 1976 the Electric Energy Society of Australia has offered encouragement to Electrical Engineering students by way of an annual cash award. The purpose of the prize is to attract interested students in pursuing a course that enables them to become engineers in the electric power distribution industry.

3. VALUE AND BENEFIT

3.1 Number of Recipients:

One (1) Recipient will be awarded the Prize each year.

3.2 Benefit/s to Recipient:

- The value of the Prize to the Recipient is \$500, and
- The Recipient will receive complimentary registration at the NSW Chapter annual conference; and
- A Certificate of Award.

3.3 Payment of benefit/s:

- The Recipient will receive one payment of \$500 by cheque or electronic funds transfer.
- The Electric Energy Society will arrange complimentary registration at the NSW Chapter annual conference.
- A Certificate of Award will be presented at the Faculty of Engineering and Information Technology prize-giving event.

4. ELIGIBILITY CRITERIA

To be eligible for the prize, Recipients must:

- Be enrolled in one of the Bachelor of Engineering or Bachelor of Engineering (Honours) suite of courses, and
- Have a major in Electrical Engineering, and
- Have successfully completed 48572 Power Circuit Theory¹, at the first attempt, in the relevant
 academic year prior to the Faculty of Engineering and Information Technology prize-giving event.

5. RECIPIENT SELECTION CRITERIA, IN PRIORITY ORDER

- The eligible student with the highest aggregate mark in 48572 Power Circuit Theory.
- In the event that two or more eligible students have the same highest aggregate mark, the following criteria will be considered:

¹ If 48572 Power Circuit Theory is not offered in the academic year and the donor nominates another subject in its place, the Prize will be awarded to the student who receives the highest aggregate mark in the nominated subject according to the eligibility and selection criteria and selection process identified in these Conditions of Award.

- Highest performance in 48572 Power Circuit Theory based on the overall aggregate mark (rounded to two decimal places) of the assessment tasks in the subject;
- Highest performance in the assessment tasks in 48572 Power Circuit Theory, based on the mark (rounded to two decimal places) of individual assessment tasks, counting back from the assessment task with the highest weighting to the assessment task with the lowest weighting;
- Highest overall academic performance by WAM in the session in which the subject was completed;
- Highest overall academic performance by WAM for the academic year in which the subject was completed.

6. SELECTION

- The Recipient with the highest final mark will be identified by the Faculty of Engineering and Information Technology Academic Administrative Officer on the basis of the selection criteria as provided in clauses 4 and 5, and confirmed by the Associate Dean (Teaching & Learning).
- In the event that two or more eligible students have the same highest final mark, the Faculty of Engineering and Information Technology Academic Administrative Officer will continue to apply the selection criteria in priority order as provided in clauses 4 and 5, and confirmed by the Associate Dean (Teaching & Learning).
- In the event that two or more eligible students are still ranked equally, a selection committee of the Dean (or nominee) (Chair) and a minimum of two (2) people will select a Recipient based on performance in 48572 Power Circuit Theory outside of formal assessment that demonstrates mastery of and engagement in the subject.
- The proposed Recipient will then by formally approved or declined by Dean, Faculty of Engineering and Information Technology (or nominee).

7. OTHER CONDITIONS

Not applicable.